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/Benjamin L. Volk, Jr./  
Benjamin L. Volk, Jr.  
Reg. No. 48,017

In re application of: Chamberlain et al. :  
: :  
Serial No.: 10/550,323 : Examiner: Fleurantin, Jean B.  
: :  
Filed: September 1, 2006 : Group Art Unit: 2162  
: :  
For: Intelligent Data Storage and  
Processing Using FPGA Devices : :

**LETTER TO PTO**

Applicant electronically filed an Amendment After Final and an IDS with references on February 23, 2011. We have today re-submitted three references after observing that the version in PAIR contained distorted text.

Favorable action is respectfully requested.

Respectfully submitted,  
**Thompson Coburn LLP**

By: /Benjamin L. Volk, Jr./  
Benjamin L. Volk, Jr.  
Reg. No. 48,017  
Thompson Coburn LLP  
One US Bank Plaza  
St. Louis, Missouri 63101-9928  
(314) 552-6352  
(314)-552-7352 (fax)